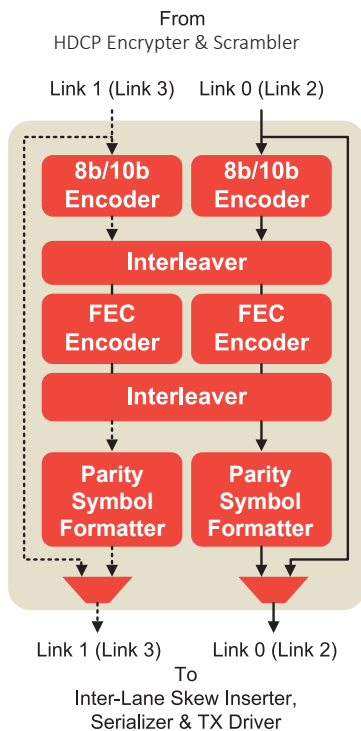


# VESA DisplayPort 1.4 FEC TX IP Core

## Applications

- GPUs
- Desktops & laptops
- UHD TVs and set-top boxes
- USB Type-C & DisplayPort products
- Tablets & mobiles

## Hardent DP1.4 FEC TX IP



## Description

The DisplayPort™ Forward Error Correction (FEC) Transmitter IP Core implements Reed-Solomon FEC and symbol interleaving as specified by the VESA DisplayPort 1.4 specification. Forward Error Correction is required to ensure glitch-free Display Stream Compression (DSC) bitstream transport.

## Key Features

- VESA DisplayPort 1.4 compliant
- Reed-Solomon RS (254, 250) FEC, 10-bit symbols
- Two-way interleaving for 1-, 2- and 4-lane modes (4-lane mode requires 2 FEC IP core instances)
- Includes the DisplayPort main 8b/10b encoder

## Deliverables

- Encrypted RTL source code IP core
- Functional and structural coverage reports
- Comprehensive integration guide
- Technical support and maintenance updates

## Product Options

- IP customization and integration services available on request
- Multi-project licenses available
- UVM verification bindable modules